

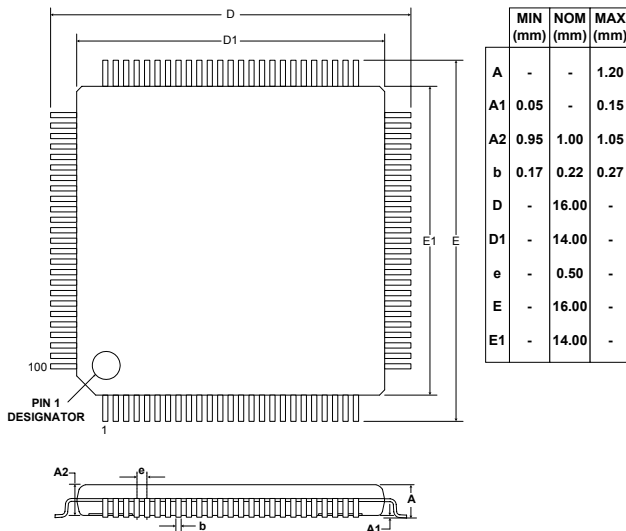


### Selected Electrical Specifications

( $T_A = -40$  to  $+85$  °C,  $V_{DD} = 2.7$  V unless otherwise specified)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
<b>GLOBAL CHARACTERISTICS</b>					
Supply Voltage		2.7		3.6	V
Supply Current (CPU active)	Clock = 25 MHz		18		mA
	Clock = 1 MHz		0.7		mA
	Clock = 32 kHz; $V_{DD}$ Monitor Enabled		20		$\mu$ A
Supply Current (shutdown)	Oscillator not running; $V_{DD}$ Monitor Disabled		0.1		$\mu$ A
Clock Frequency Range		DC		25	MHz
<b>16-BIT A/D CONVERTERS</b>					
Resolution			16		bits
Integral Nonlinearity	Single-ended Mode		$\pm 0.75$	$\pm 2$	LSB
	Differential Mode		$\pm 0.50$	$\pm 1$	LSB
Differential Nonlinearity	Guaranteed Monotonic		$\pm 0.5$	$\pm 1$	LSB
Signal-to-Noise Plus Distortion	$F_{in} = 10$ kHz, Single-ended		86		dB
	$F_{in} = 10$ kHz, Differential		89		dB
Total Harmonic Distortion	$F_{in} = 10$ kHz, Single-ended		96		dB
	$F_{in} = 10$ kHz, Differential		103		dB
Spurious-Free Dynamic Range	$F_{in} = 10$ kHz, Single-ended		97		dB
	$F_{in} = 10$ kHz, Differential		104		dB
Throughput Rate				1	Msp/s
Input Voltage Range	Single-ended ( $A_{INn} - A_{INnG}$ )	0		$V_{REF}$	V
	Differential ( $A_{IN0} - A_{IN1}$ )	$-V_{REF}$		$V_{REF}$	V
Power Supply Current (each ADC)	Operating Mode, 1 Msp/s ( $AV_{DD} + AV+$ )		5.5		mA
	Shutdown Mode		1		$\mu$ A

### Package Information



### C8051F060DK Development Kit

